

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: M. Horiguchi, et al.

Serial No.: (New continuation application under 37 CFR §1.53[b] of  
U.S. Application Serial No. 10/401,975)

Filed: On Even Date Herewith

For: A SEMICONDUCTOR DEVICE HAVING REDUNDANCY CIRCUIT

Group: 2824

Examiner: J. HUR

**CLAIM FOR PRIORITY**

**MS: PATENT APPLICATIONS**

Commissioner For Patents  
POB 1450  
Alexandria, VA 22313-1450

March 31, 2004

Sir:

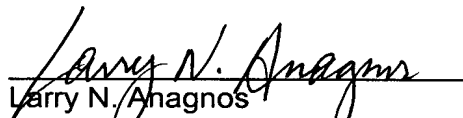
Pursuant to 35 USC §119 and 37 CFR §1.55, applicants hereby claim the right of priority based on the following foreign applications filed in Japan:

**Application No. 63-252028 filed October 7, 1988**

**Application No. 63-275375 filed October 31, 1988.**

Certified copies of these two (2) foreign applications were filed on November 23, 1990 in U. S. application Serial No. 07/419,399, filed on October 10, 1989, the earliest one in the line of prior applications for which benefit has been requested under 35 U.S.C. §120.

Respectfully submitted,  
**ANTONELLI, TERRY, STOUT & KRAUS, LLP**

  
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